

TA900 Program and Debug Box

Application Information

The TA900 is an Interface Box which can be used to program and debug hardware modules providing a corresponding connector.

The Interface Box connects to compatible modules via a 20-pin Flexible Printed Circuit (FPC) Connector which can provide access to the module's JTAG Chain and the additional interfaces A and B. The TA900 can be accessed by USB 2.0 and by a 14-pin JTAG Header and is equipped with a Pushbutton Switch offering the possibility to send an impulse to one of the connected module's I/O pins. A green LED indicates the assertion of this impulse by interrupting illumination.



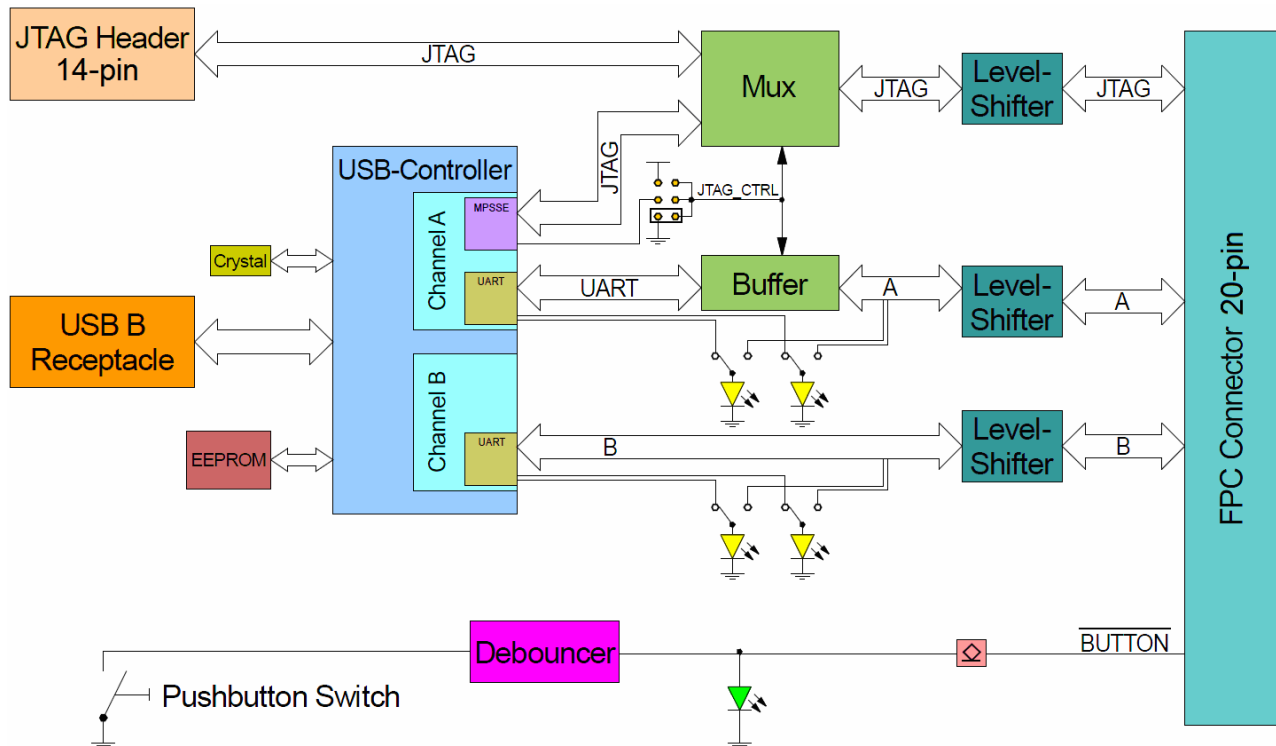
In case of the user programmable FPGA boards TAMC631 and TAMC640/641, Interface A provides access to the UART of the onboard Module Management Controller (MMC) and Interface B connects to two user pins of the module's onboard FPGA. If a UART core is implemented in the module's FPGA serial communication via Interface B is possible.

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Technical Information

- Form Factor: Interface Box
 - Size: 139.7mm x 82.6mm x 26.2mm
- 20-pin Flexible Printed Circuit (FPC) Connector
 - JTAG Interface
 - Interface A (UART / MPSSE Mode JTAG)
 - Interface B (UART)
 - User I/O pin BUTTON
- I/O Voltages: 1.2V to 3.3V
- FTDI Chip FT2232H USB to UART Controller
- USB B Receptacle
 - USB 2.0 High Speed and Full Speed
 - Self-Powered
- 14-pin Shrouded JTAG Header
 - Matches the "Xilinx Platform Cable USB II" pinout
- Operating temperature -30°C to +75°C
- MTBF (MIL-HDBK217F/FN2 G_B 20°C)
TA900-10R: 639000 h

Order Information

RoHS Compliant

TA900-10R Program and Debug Box, USB and JTAG Connectors, extended temperature range

Documentation

TA900-DOC User Manual