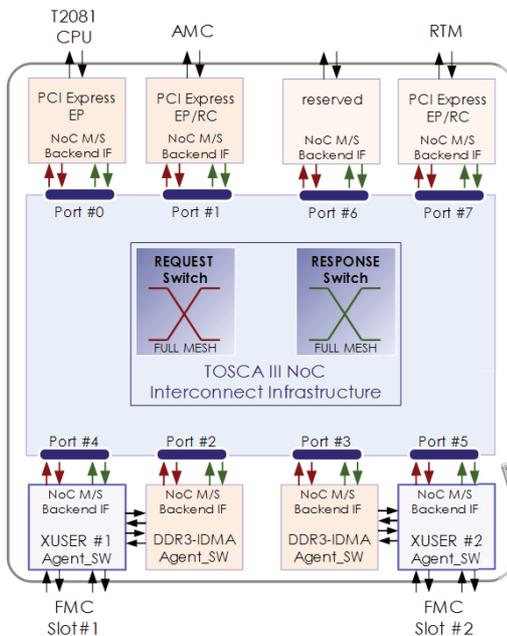


## IFC\_1410 QorIQ T2081 & Kintex UltraScale AMC Building the Next Generation of Control Systems

Data Sheet



### Key Features

- MTCA.4 mid-size double-width AMC form factor
- NXP QorIQ T2081 CPU @ 1.8 GHz
  - ✓ Four dual-threaded e6500 computing cores
  - ✓ On-board 2 GB DDR3L-1866 SDRAM
  - ✓ High-speed peripheral interfaces supporting PCI Express Gen3 and 10 Gbps Ethernet
  - ✓ AltiVec technology-based SIMD engine
  - ✓ Non-volatile boot media: NOR and SPI flash
  - ✓ Non-volatile storage memory: 4 Gbit NAND flash
- Xilinx Kintex UltraScale Central FPGA:
  - ✓ High end Xilinx Kintex UltraScale KU040 (default) or KU060 FPGA
  - ✓ 1024 MB dual channel DDR3L-1066 SDRAM
  - ✓ Local (SPI flash) and remote (Ethernet) configuration support
  - ✓ Up to three embedded PCI Express blocks configurable as End Point or Root Complex
  - ✓ Powered by TOSCA III FPGA Design Kit for straight-forward FMC integration and customization
- Dual HPC VITA 57.1 compliant FMC slots
- Ultra low jitter clock inputs on front panel, AMC and RTM interfaces
- MTCA.4 D1.4-compliant RTM interface
- Total integration within EPICS ecosystem

### Overview

IOxOS Technologies unveils the IFC\_1410, an intelligent FMC carrier in AMC form factor that is the cornerstone of its comprehensive MTCA.4 ecosystem based on:

- MTCA.4 line of AMCs and  $\mu$ RTM modules, including CPUs, FMC carriers, Fast Digitizers and communication extenders
- High-end set of ADC and DAC modules in FMC form factor
- FPGA Design Kits for faster integration and customization
- EPICS support

The IFC\_1410 integrates the latest generation of NXP PowerPC QorIQ processors. The T2081 provides dual-threaded quad-core capability running at up to 1.8 GHz with medium power operation (~14[W] Typ. @ 1.4 GHz), and is complemented by large DDR3L System Memory (up to 4 GBytes), non volatile memory NOR, NAND and multiple I/O capabilities such as dual 1000-BASE-KX Ethernet, 10 Gigabit Ethernet and dual PCI Express Gen2 and Gen3.

The on-board Xilinx Kintex UltraScale FPGA is powered by IOxOS Technologies' FPGA Design Kit (TOSCA series), that enables the straight-forward integration of the FMC modules and the implementation of custom applications within a high-performance Network on Chip (NoC) based architecture.

An extensive EPICS ecosystem of open source tools, libraries and applications is growing around these MTCA.4 and FMC COTS, with the invaluable collaboration of the Paul Scherrer Institut (PSI) in Switzerland, aiming to support the physics community in the development of efficient distributed real-time platforms for precision instrumentation and state of the art accelerator control systems.

powerBridge  
Computer

## Product Overview

The IFC\_1410 Intelligent FMC Carrier is a mid-size (4HP) double-width MTCA.4-compliant AMC unit featuring two HPC VITA57.1-compliant FMC slots. The two FMC slots are controlled by a Xilinx Kintex UltraScale FPGA device, connected to the AMC interface through one PCIe x4 Gen3 link (ports 4 to 7), point-to-point LVDS links (ports 12 to 15) and multi-point M-LVDS links (ports 17 to 20), to a NXP QorIQ T-series T2081 processor with a PCIe x4 Gen 3 link, and to the RTM interface through up to three GTP x4 links.

The processor is connected by one 1000-BASE-KX Gigabit Ethernet link to the AMC interface (port 0), and 1000-BASE-KX Gigabit Ethernet link (and two 10 Gigabit Ethernet links) to the ADF Rear Transition Module (RTM) interface.

This versatile AMC unit can also be used as a standalone CPU module.

## Kintex UltraScale Central FPGA

The IFC\_1410 Central FPGA is intended for user defined applications, providing FMC VITA57.1 interfacing resources. This FPGA is a major upgrade from the previous family based on Virtex-6T devices.

Two DDR3L memory devices are directly connected to the Central FPGA, and are fully supported by TOSCA III FDK (2 Bank @ 3.2 GBytes/s with 2/4 IDMA).

## A Set of ADC/DAC and GPIO FMC Modules

The IFC\_1410 FMC Carrier takes full advantage of IOxOS Technologies comprehensive line of ADC/DAC and GPIO modules in FMC form factor to increase its capabilities for data acquisition and control applications.

These FMCs include the following products:

- **ADC\_3110/3111 Fast ADC:** 8 channels ADC 16-bit @ 250 Msps (AC / DC coupling)
- **ADC\_3112 Ultra-Fast ADC:** 4 channels ADC 12-bit @ 1 Gsps or 2 channels ADC 12-bit @ 2 Gsps (DC coupling)
- **DAC\_3113 Fast DAC:** Dual channel ADC 16-bit @ 250 Msps & Dual channel DAC 16-bit @ 250 Msps (DC coupling)
- **ADC\_3117 High-Density ADC:** 20 channels ADC 16-bit @ 5 Msps & 2 channel DAC 16-bit @ 1 Msps (SE or Differential inputs)
- **DIO\_3118 High-Density Digital I/O:** 16 TTL/LVDS programmable inputs and 16 TTL/LVDS programmable outputs

## Digital RTM Interface

The IFC\_1410 complies with MTCA.4 class D1.4 RTM pinout assignment recommendation providing 8 LVDS I/O, 3 LVDS outputs, up to 12 GTP high-speed lanes connected with Central FPGA, 2 high-speed lanes connected to the CPU, 1 SGMII interface and 2 LVDS low phase noise clocks.

## TOSCA III FPGA Design Kit

Conventional FPGA design environments offer a set of IP Cores along with implementation examples. IOxOS Technologies goes one step further releasing the **TOSCA III FPGA Design Kit**, a comprehensive system design environment optimized for Kintex UltraScale devices that covers all the path, from the SW application to the FPGA user code.

The TOSCA III FPGA Design Kit is delivered with full VHDL source code together with a set of test-benches and Bus Functional Models (BFM) to set up a complete VHDL simulation environment for functional verification purposes.

The TOSCA III architecture is based on a PCI Express switch centric structure implementing a memory mapped model with segregated I/O Space (CONTROL Plane) and Memory Space (DATA Plane).

The TOSCA III FPGA Design Kit enhances the versatility of the IFC\_1410 AMC board, providing the user with a powerful tool for the direct integration of FMC modules and for the implementation of custom applications within the IFC\_1410 on-board Kintex UltraScale FPGA. This solution also makes possible a significant reduction of the FPGA development time, by allowing users to focus on their specific application and providing:

- access to IOxOS Technologies IP library
- a user area (XUSER) with a dedicated simulation environment
- reference designs

To match PCIe Gen3 performance requirements, the embedded NoC switch is enhanced to support 128-bit data paths.

TOSCA III FDK is fully integrated within Xilinx CAE Tools (Vivado 2015.2 and later).

## XUSER Specific Applications

The TOSCA III infrastructure is backward compatible with TOSCA II FDK used with the IFC\_1210/1211. This means that all XUSER specific applications designed for the IFC\_1210/1211 VME64x Single Board Computers can be used within the new IFC\_1410 TOSCA III environment.

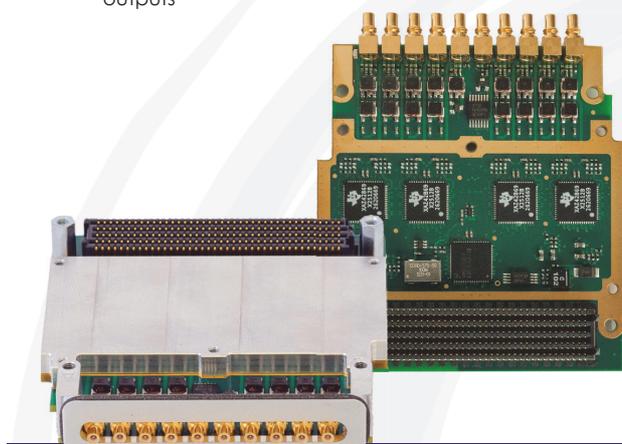
## EPICS Support

The whole MTCA.4 ecosystem (AMCs and  $\mu$ RTM modules, ADC/DAC FMCs and TOSCA III FPGA Design Kit) is fully integrated within the EPICS environment by means of open source tools, libraries and applications developed by the Paul Scherrer Institut (PSI) in Switzerland.

## Ordering Information

Article Reference	Product Description
IFC_1410-A0	MTCA.4 NXP T2081 Single Board Computer AMC with Xilinx Kintex UltraScale XCKU040
TOSCAIII_FDK-SP	TOSCA III FPGA Design Kit – Starter Package (10 licenses + 3-days hands on workshop)
TOSCAIII_FDK-DP	TOSCA III FPGA Design Kit – Development Package (10 additional licenses)

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