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# Hermes RCx - Series

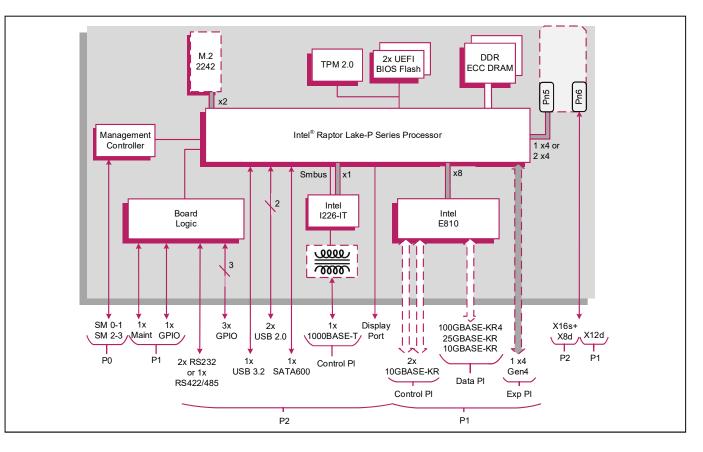
# Rugged 3U VPX<sup>™</sup> I/O Intensive Plug In Card (PIC) based on 13<sup>th</sup> Generation Intel<sup>®</sup> Core<sup>™</sup> Series Processor

# **Key Features**

Hermes is a rugged 3U VPX<sup>™</sup> Plug In Card (PIC) based on the 13<sup>th</sup> Gen Intel<sup>®</sup> Core<sup>™</sup> Processor for general purpose computer applications. It is designed in alignment with the SOSA<sup>™</sup> Technical Standard for I/O intensive processor PICs.

- 14-core processor for high performance
- 100GBASE-KR4 Ethernet Data plane
- x4 Gen 4 PCI Express<sup>®</sup> Expansion plane for high speed communication with adjacent board(s)
- XMC Site for additional I/O resources
- Optional M.2 module for storage with Write/Protect and Opal 2.0 compliance
- Rugged conduction-cooled and Air Flow Thru versions available





# CONCURRENT

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# **Specification**

#### **VPX Processor PIC**

- rugged conduction-cooled 3U VPX<sup>™</sup> PIC based on 13th Generation Intel<sup>®</sup> Core<sup>™</sup> Series Processor
- compliant with two OpenVPX<sup>™</sup> module profiles:
  - → SLT3-PAY-1F1F2U1TU1T1U1T-14.2.16
  - → MOD3-PAY-1F1F2U1TU1T1U1T-16.2.15-4

#### **Central Processor**

- 14-core Intel<sup>®</sup> Core<sup>™</sup> i7-13800HRE Processor
- Intel Vector Neuro Network Instructions (VNNI)
- Intel<sup>®</sup> Iris<sup>®</sup> Xe (Gen 12) Graphics Engine with up to 96 EUs

# DRAM

- 64 Gbytes soldered LPDDR5 IBECC DRAM:
  - → in-band ECC
  - → single bit error correction

# **Optional XMC Site**

- 1x XMC site, in a single VPX slot (VITA 42.0):
  - → XMC rear I/O, providing X12d+x16s+X8d
  - → 1 x4 or 2 x4 PCI Express<sup>®</sup> (PCIe<sup>®</sup>)
  - → PCIe Gen 1, Gen 2 and Gen 3
- XMC connector type (build option):
  - → up to Gen 2, VITA 42 XMC (black color)
  - → up to Gen 3, VITA 61 XMC 2.0 (white color)
- XMC VPWR +12 V
- VITA 46.9 XMC I/O pin-out

# Serial Ports

- 1x RS232/422/485 port accessed via P2
- 1x maintenance port accessed via P1
- Maintenance port on P1 supports LVCMOS levels
- 16550 compatible UARTs

# Graphics/Audio Interfaces

- 1x graphics/audio interface:
  - → DisplayPort<sup>TM</sup> v1.2/1.4 interface, supporting audio and video, via P2
  - → up to 5120 x 3200 @ 60 Hz, driver dependent

# **Other Peripheral Interfaces**

- PC RTC, long duration timer, watchdog timer
- 2x USB 2.0 and 1x USB 3.2 (Gen 1) ports via
- P2 3x GPIO signals via P2
- 1x GPIO signal via P1

# Mass Storage Interfaces

- 1x SATA 600 via P2
- 1x M.2 SSD site supports:
  - → 2242 format module
  - → x2 PCIe interface (M-key)
  - Opal 2.0 security encryption
  - → Write Protect
  - → NVM Express<sup>®</sup> (NVMe<sup>®</sup>) logical device interface

# VPX Control Plane, Ethernet

- configurable Control Plane (VITA 46.6)
- 1x 2.5GBASE-T Ethernet port via P2:
  - → supports 10/100/1000BASE-T
  - → option for with or without magnetics
  - implemented by Intel Ethernet Controller I226-IT
  - → TSN support
- up to 2x 10GBASE-KR Ethernet ports via P1 (VITA 46.7):
  - → supports up to 2x 10GBASE-KR
  - → implemented by Intel<sup>®</sup> Ethernet Controller E810 via x8 PCle
  - factory build option available to disable Control Plane
- supports IEEE 1588 Precision Time Protocol

# VPX Data Plane, 100 Gigabit Ethernet

- configurable Ethernet VPX Data Plane fabric interface (VITA 46.7)
- 1x 100 Gigabit Ethernet port via P1 (VITA 46.7):
  - → supports 1x 100GBASE-KR4 or 1x 25GBASE-KR or 4x 10GBASE-KR
  - implemented by Intel Ethernet Controller E810 via x8 PCIe
  - factory build option available to disable Data Plane
- supports IEEE 1588 Precision Time Protocol

# VPX Expansion Plane, PCI express

- configurable PCI Express (PCIe) VPX Expansion Plane fabric interface (VITA 46.4):
  - → 1 x4 Gen 4
  - → factory build option available to disable Expansion Plane
- PCIe interfaces support Gen 1, Gen 2, Gen 3 and Gen 4

# **Optional Built-In Test (BIT) Support**

Power-on BIT, Initiated BIT, Continuous BIT

# System Management

- VITA 46.11 IPMC on-board controller:
  - → SM0-1 and SM2-3
  - CPU temperature and voltage monitor accessed via System Management interface
- option for VITA 46.11 compatible Tier 1 Chassis Manager

# **Board Security Packages**

- Trusted Platform Module (TPM 2.0)
- supports Total Memory Encryption, ROP Attack Prevention and Advanced Crypto-Key Protection
- option for Sanitization Utility Software Package
- option for proprietary board-level security features

#### Software Support

Please contact your local Concurrent Technologies sales office for further details on board build options and accessories.

supports Linux<sup>®</sup> and Windows<sup>®</sup>

# Firmware Support

- dual 32 Mbyte BIOS SPI Flash EPROMs
- UEFI boot firmware (BIOS):
  UEFI 2.7 support
  - → implements Secure Boot
- implements Intel Boot Guard
- optional Fast Boot solution using the Intel Firmware Support Package (FSP)
- LAN boot firmware included

# Safety

 PCB (PWB) manufactured with flammability rating of UL94V-0

# **Electrical Specification (Estimated)**

- typical current figure for Intel<sup>®</sup> Core<sup>TM</sup> i7-13800HRE Processor with 64 Gbytes DRAM:
  - → +12 V VS1 @ TBD A
  - → +3.3 V AUX @ TBD A
- +12 V AUX and -12 V AUX routed to XMC site

→ VITA 47 Class CC3, -40°C to +70°C (RCx-

→ VITA 47 Class C4, -55°C to +105°C

→ -1,500 to 60,000 feet (-460 to 18,300

→ from 8,000 to 60,000 feet (from 2440 to

relative humidity: 5% to 95%, non-condensing

3U VPX form-factor (VITA 46.0, VITA 48.0)

3.9-inches x 6.3-inches (100 mm x 160 mm)

→ 1.0-inch VPX-REDI Type 1, RCR-Series,

Type 1 Extended Covers Two Level

connectors to VITA 46.0 for P0, P1 and P2

→ random vibration - VITA 47 Class V3,

Datasheet Code 1866/Mar23 © Concurrent Technologies 2023 – Proprietary information

shock - VITA 47 Class OS2, 40 g

■ +5 V and +3.3 V are not connected

**Environmental Specification** 

operating temperature at card edge:

conduction-cooled (VITA 48.2)

non-operating temperature:

Series)

operating altitude:

meters)

rapid decompression:

18,300 meters)

slot width (VITA 48.0):

operating mechanical:

0.1 g²/Hz

**Mechanical Specification** 

Maintenance (VITA 48.2)