

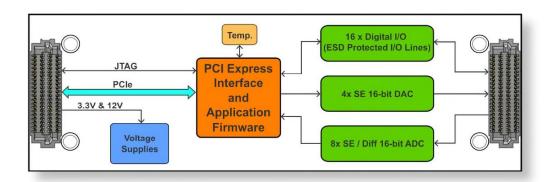
Ehlbeek 15a 30938 Burgwedel fon 05139-9980-0 fax 05139-9980-49

www.powerbridge.de info@powerbridge.de

I/O Company



TQMC701 8 Single-Ended / Differential A/D Channels, 4 D/A Channels and 16 Digital I/O Channels



TQMC701 Block Diagram

Application Information

The TQMC701 is a VITA 93.0 compatible single-width QMC providing 8 channels of simultaneous sampling single ended or true differential bipolar analog inputs, 4 channels of simultaneous update analog voltage output and 16 ESD-protected 5 V tolerant TTL digital I/O lines.

The 16 bit analog input channels support per channel software configurable input modes and voltage ranges. Each channel can be operated in bipolar single-ended, unipolar single-ended and bipolar differential mode. In the single-ended modes it offers software selectable input voltage ranges of 0-5 V, 0-10 V, 0-12.5 V, ±2.5 V, ±5 V, ±6.25 V, ±10 V and ±12.5 V. In differential mode the input voltage ranges are selectable between ±5 V, ±10 V, ±12.5 V and ±20 V. Sampling rate for all channels active is up to 1 Msps.

The 16 bit analog output channels support per channel software configurable output voltage ranges of 0-5 V, 0 10 V, 0-10.8 V, ±5 V, ±10 V or ±10.8 V. The conversion time is typ. 10 µs and the DAC outputs are capable to drive a load of 2 k Ω , with a capacitance of up to 4000 pF.

All 16 digital I/O lines are ESD-protected and 5 V-tolerant. Every I/O line is individually programmable as input or output if not used for external synchronization. TTL I/O lines can be set to high, low, or tristate.

Every TQMC701 is factory calibrated. The calibration information is stored in a user-accessible on-board serial EEPROM individual to every module. The correction data values may be used to perform a hardware correction for A/D channels per input range and D/A channels per output range during functional operation.

In addition, there is a temperature sensor on every module to allow supervisory and data temperature coherence evaluation.

The TQMC701 provides different operation modes to perform manual and automatic A/D and D/A conversions.

For automatic conversions there are dedicated A/D and D/A sequencer units. These include on-board data buffer and DMA controller for A/D data transfer, D/A data fetch and conversion rate generators.

Sequencers provide optionally a Frame Mode for repetitive frames of A/D and D/A conversions upon an internal or external trigger signal event.

Conversion clock (conversion rate) and frame trigger signals may be generated on-board for internal use. Optionally these can be provided via the I/O connector if the card is operating as a master card in a Multi-Board configuration (externally synchronization). The conversion clock (conversion rate) and frame trigger signals may also be sourced externally to be used internally.

The TQMC701 is available as air cooled and conduction cooled variant.



Conduction Cooled QMC

TEWS Technologies GmbH keeps the right to change technical specification without further notice. All trademarks mentioned are property of their respective owners.

Issue 1.0.0 19.05.2025

TEWS Technologies GmbH

Eggerstedter Weg 14, 25421 Pinneberg, Germany

WEEE-Reg.-Nr. DE 23 438 792

IBAN DE42 2519 0001 0014 4789 01 (USD)



The Embedded I/O Company

Technical Information

- O Form Factor: Standard single QMC conforming to VITA 93.0
 - O Board size: 78.25 mm x 26 mm
- O PCI Express 2.1 compliant interface
- FPGA based PCIe endpoint
- O Digital I/O
 - O 16 ESD-protected 5 V-tolerant TTL lines
 - O Direction individually programmable
- O 8 channels 16 bit analog input
 - Simultaneous sampling
 - O differential or single-ended inputs
 - Programmable input voltage (one setting for all channels):
 0-5 V, 0-10 V, 0-12.5 V,
 ±2.5 V, ±5 V, ±6.25 V, ±10 V, ±12.5 V
 - O Sampling rate: 1 Msps
 - O Overvoltage protection
 - Factory calibration

- 4 channels single-ended 16 bit analog output
 - Simultaneous update
 - O Programmable output voltage: 0-5 V, 0-10 V, 0-10.8 V, ±5 V, ±10 V, ±10.8 V
 - O Conversion time: typ.10 µs
 - O Up to 2 kΩ resistive, 4000 pF capacitive load
 - Overcurrent protection
 - Factory calibration
- O Operating temperature -40 °C to +85 °C

Order Information

RoHS Compliant

TQMC701-10R-A 8 Analog Inputs (16 bit), 4 Analog Outputs (16 bit), 16 TTL I/O, air cooled

TQMC701-10R-H 8 Analog Inputs (16 bit), 4 Analog Outputs (16 bit), 16 TTL I/O, conduction cooled

For the availability of non-RoHS compliant (leaded solder) products please contact TEWS.

Software

TDRV019-SW-25 Integrity Software Support
VxWorks Software Support
VxWorks Software Support
Windows Software Support
Linux Software Support
VxWorks Software Support
Control of the control

For other operating systems please contact TEWS.

Related Products

TPCE210 2 Site QMC Carrier, PCIe x4, Gen2, low-profile, VHDCI-68 I/O

TEWS Technologies GmbH keeps the right to change technical specification without further notice. All trademarks mentioned are property of their respective owners.

Issue 1.0.0 19.05.2025

IBAN DE42 2519 0001 0014 4789 01 (USD)